



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Om P. Agrawal; Hans W. Klein  
 Assignee: Lattice Semiconductor Corporation  
 Title: CLOCK GENERATOR  
 Serial No.: 10/629,223      Filing Date: July 29, 2003  
 Examiner: Cox, Cassandra F.      Group Art Unit: 2816  
 Docket No.: M-15169 US      Confirmation No. 5335

Irvine, California  
January 23, 2006

**Fax No.:** 1-571-273-2885

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I hereby certify that the following are being facsimile transmitted to the Patent and Trademark Office on the date shown below.

1. Part B – Fee Transmittal (1 page)
2. Fee Address “Indication Form” (1 page)
3. Change of Correspondence Address (1 page)

Dated: January 23, 2006

Tina Kavanaugh

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January 23, 2006

**MAIL STOP ISSUE FEE**  
 Commissioner For Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

Re:	Applicant(s):	Om P. Agrawal; Hans W. Klein
Title:	Clock Generator	
Serial No.:	10/629,223	
Examiner:	Cox, Cassandra F.	
Docket No.:	M-15169 US	
	Filed:	7/29/2003
	Group Art Unit:	2816
	Confirmation No.	5335

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

1. Facsimile Cover Sheet (1 page)
2. Part B – Fee Transmittal (1 page)
3. Fee Address “Indication Form” (1 page)
4. Change of Correspondence Address (1 page)

Certificate of Mailing

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 Tina Kavanaugh

January 23, 2006

Respectfully submitted,



Greg J. Michelson  
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